

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("20020194564").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/29 16:39
L2	1	L1 and boundary scan test	US-PGPUB; USPAT	ADJ	OFF	2006/01/29 16:39
L3	5	(("20010194564") or ("6519728") or ("6400173") or ("20020171449") or ("20030006795") or ("20030032263")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/29 16:39
L5	5	(("6261467") or ("5933709") or ("4835593") or ("6255729") or ("6456099")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/29 16:39
L6	3	(US-20040195672-\$ or US-20040197941-\$).did. or (US-6734549-\$).did.	US-PGPUB; USPAT	OR	OFF	2006/01/29 16:40
L7	2659	(714/724,726,727).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/29 16:40
L8	1081	boundary scan test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/29 16:40
L9	126	L8 and internal scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/29 16:40
L10	114	L9 and (board pcb mount mounting substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/29 16:40
L11	86	L10 and (wire wiring bond bonding)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L12	305	L8 and L7	US-PGPUB; USPAT; USOCR	ADJ	OFF	2006/01/29 16:40
L13	3	L3 and L8	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/29 16:40
L14	2	L3 and L11	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/29 16:40

L15	1044	(257/48).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/29 16:40
L16	32	L11 and @pd<"20010702"	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L17	71	L11 and chip	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L18	32	L16 and @pd<"20010702"	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L19	19	L8 and internal scan chain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/29 16:40
L20	16	L19 and (board pcb mount mounting substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/29 16:40
L21	9	L20 and (wire wiring bond bonding)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L22	9	L21 and input and output	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/29 16:40
L23	7	L21 and input terminal and output terminal	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/01/29 16:40

xreferplus



Hargrave's Communications Dictionary, Wiley
from Wiley

This entry was brought to you by US Patent and Trademark Office.

printed circuit

A pattern of electrical conductors applied to one or more layers of a substrate (called a printed circuit board or PCB). The conductors electrically interconnect devices physically mounted to the surface(s) of the substrate. (A printed circuit board with components installed is called a printed circuit assembly.)

Copyright © 2001 by the Institute of Electrical and Electronics Engineers, Inc.

Citing this entry

printed circuit. Hargrave's Communications Dictionary, Wiley (2001). Retrieved 29 January 2006, from xreferplus.
<http://www.xreferplus.com/entry/2723893>